

FIG. 1

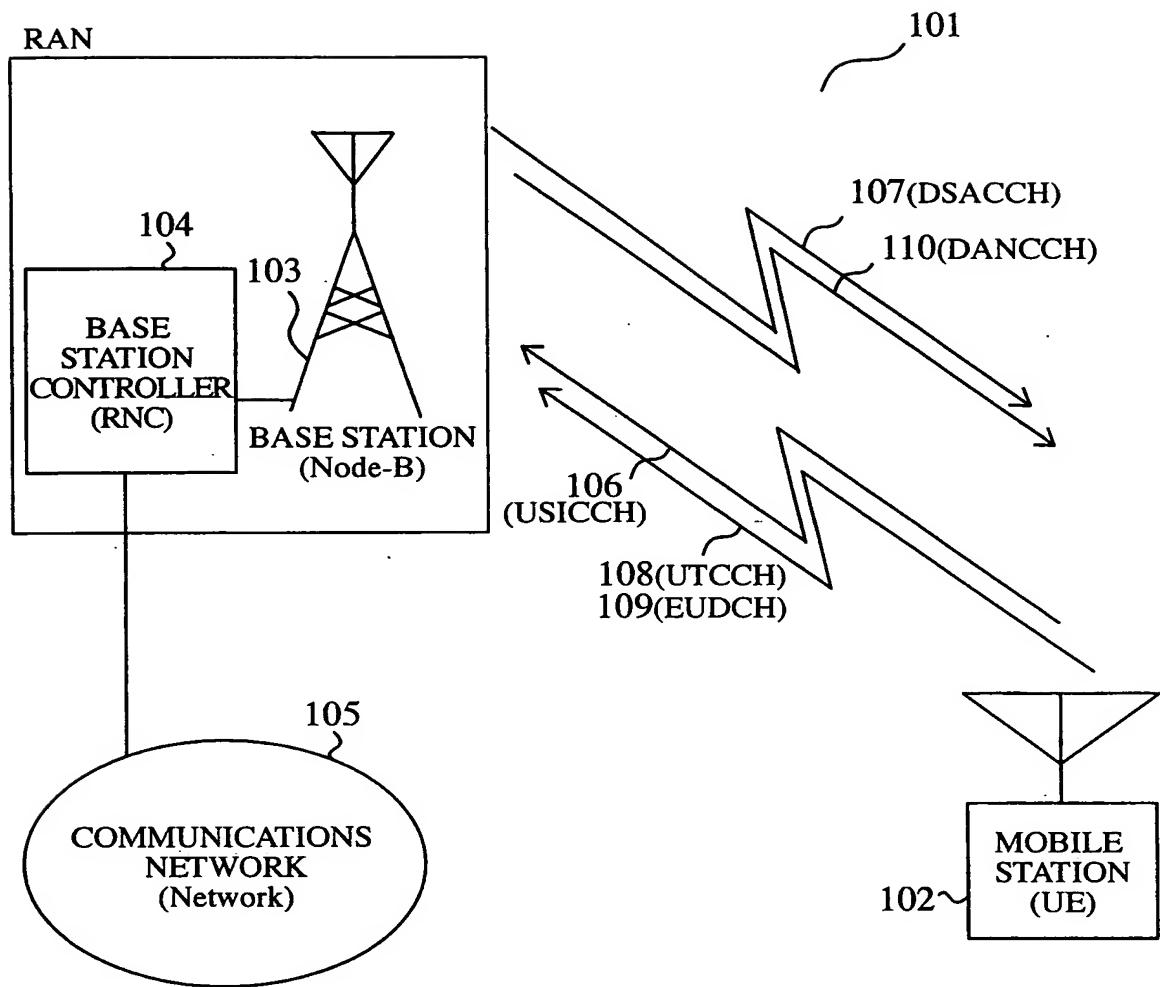


FIG.2

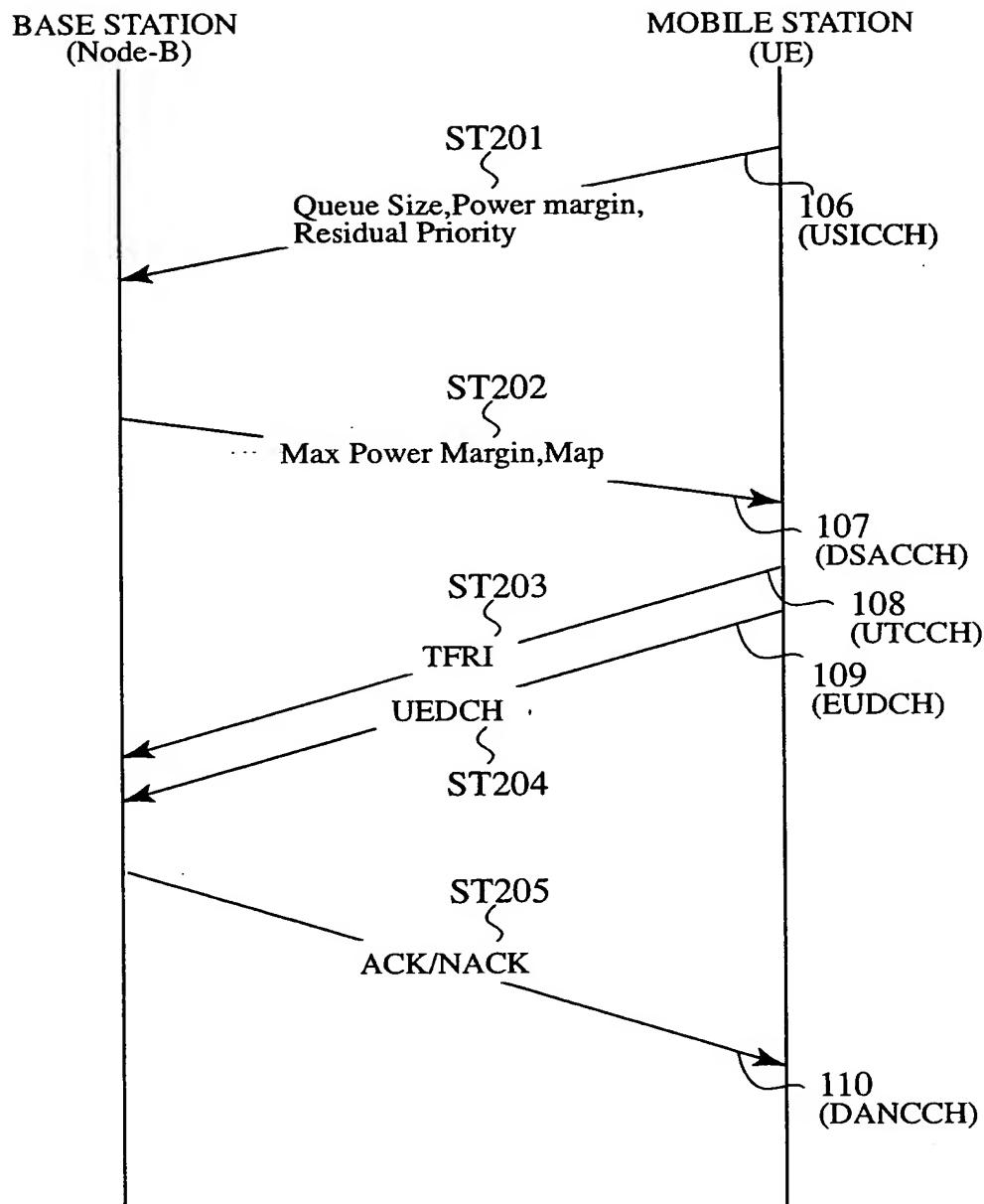


FIG.3

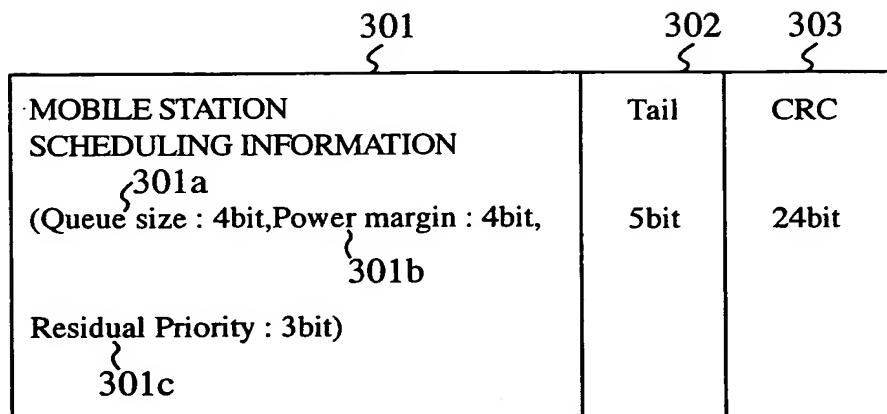


FIG.4

102

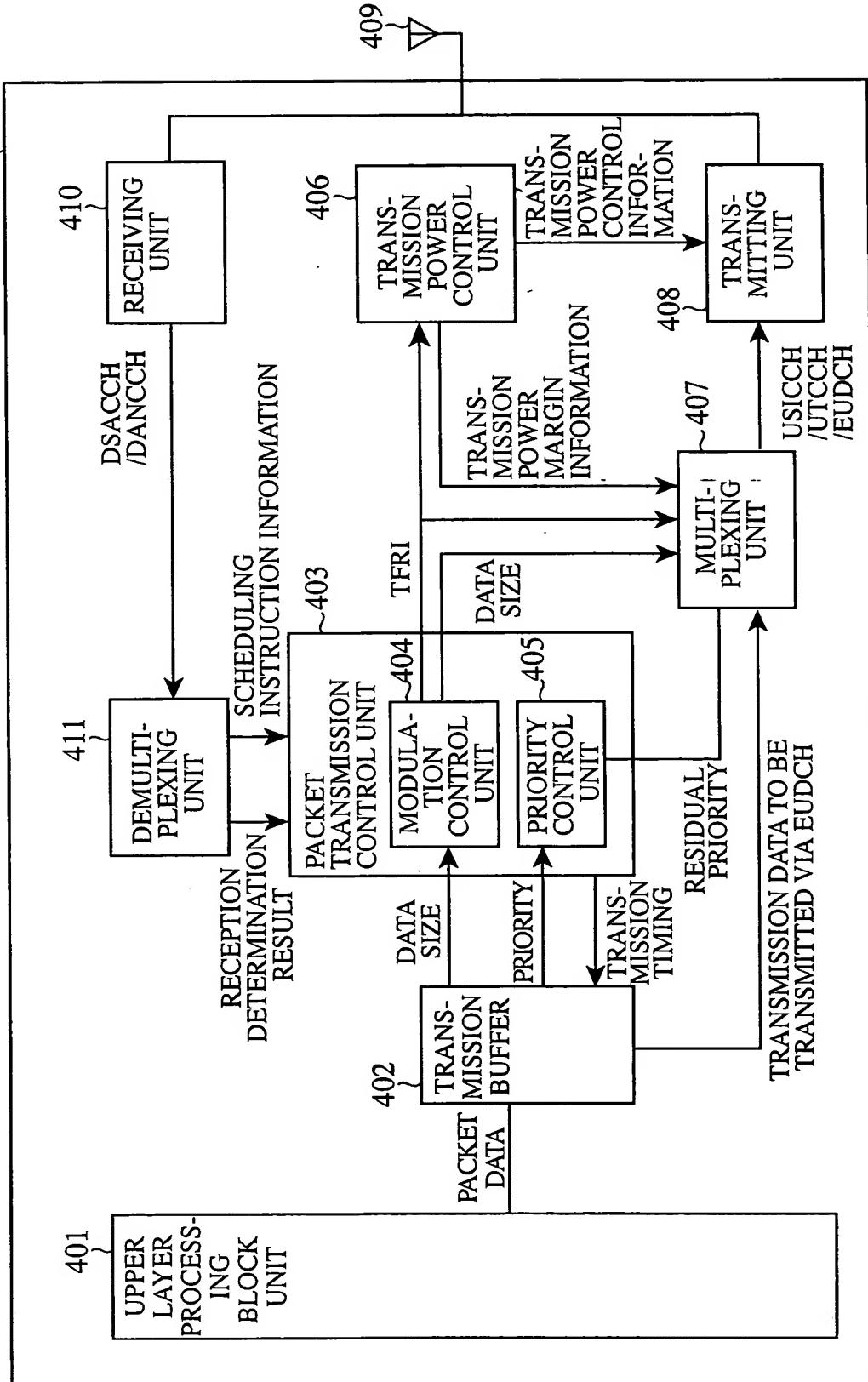


FIG.5

103

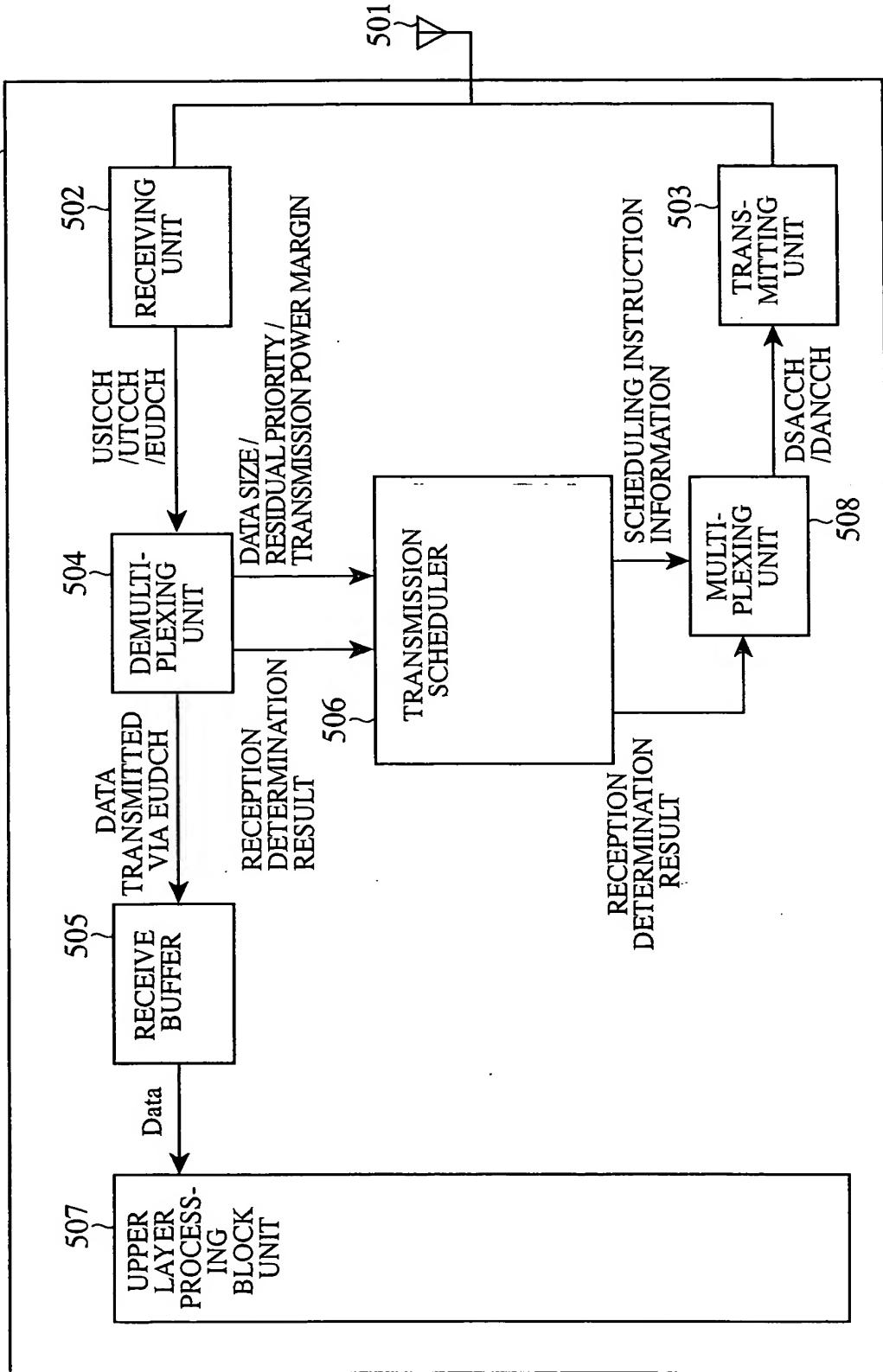


FIG.6

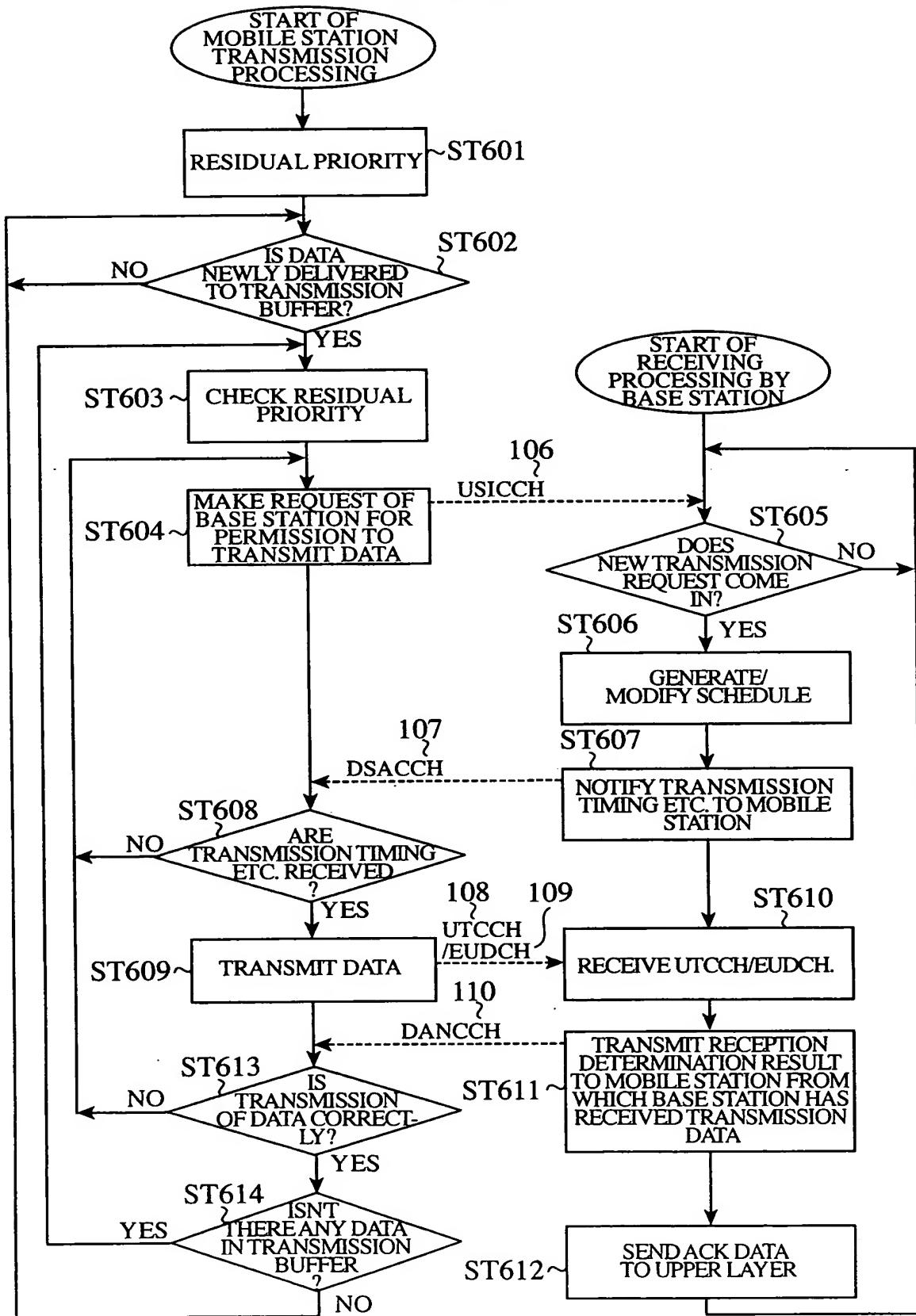


FIG.7

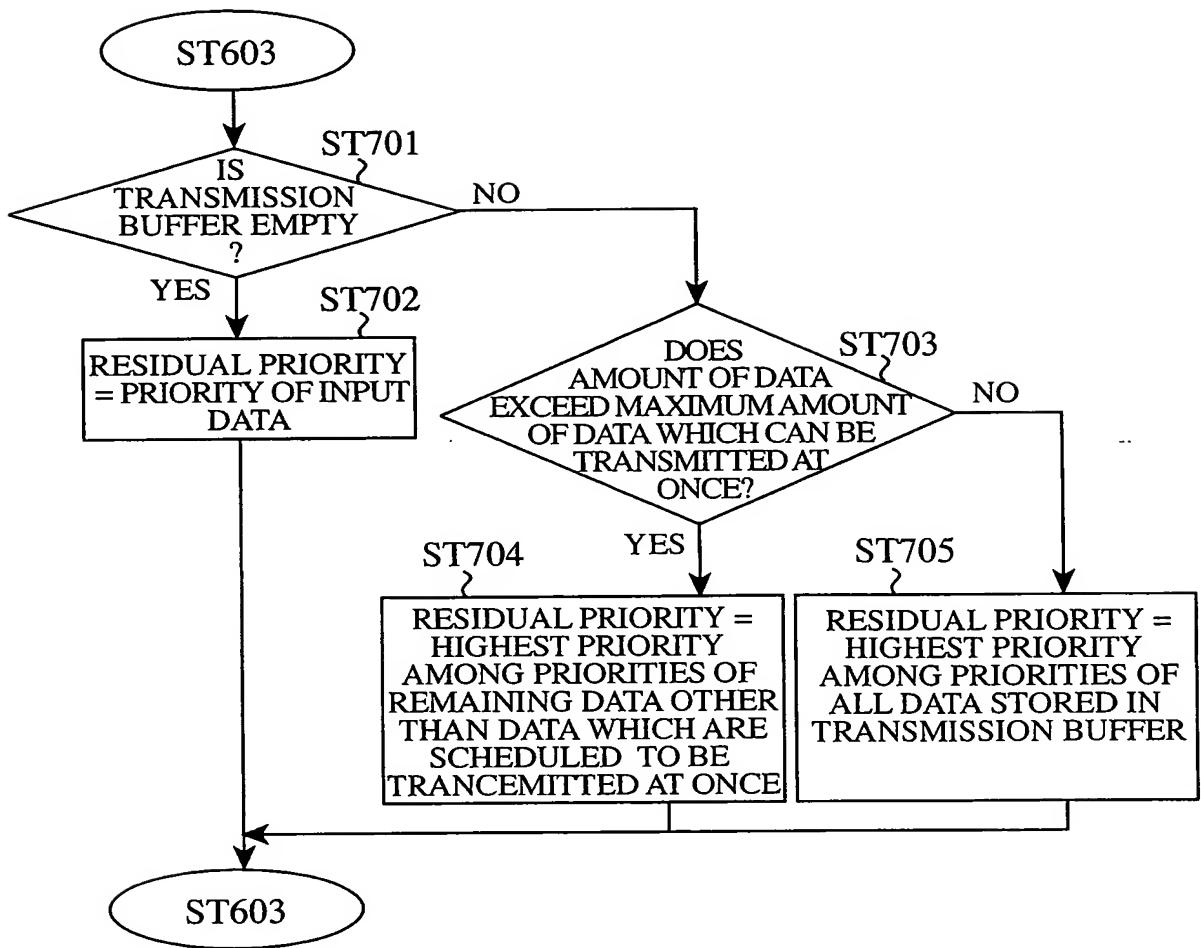


FIG.8

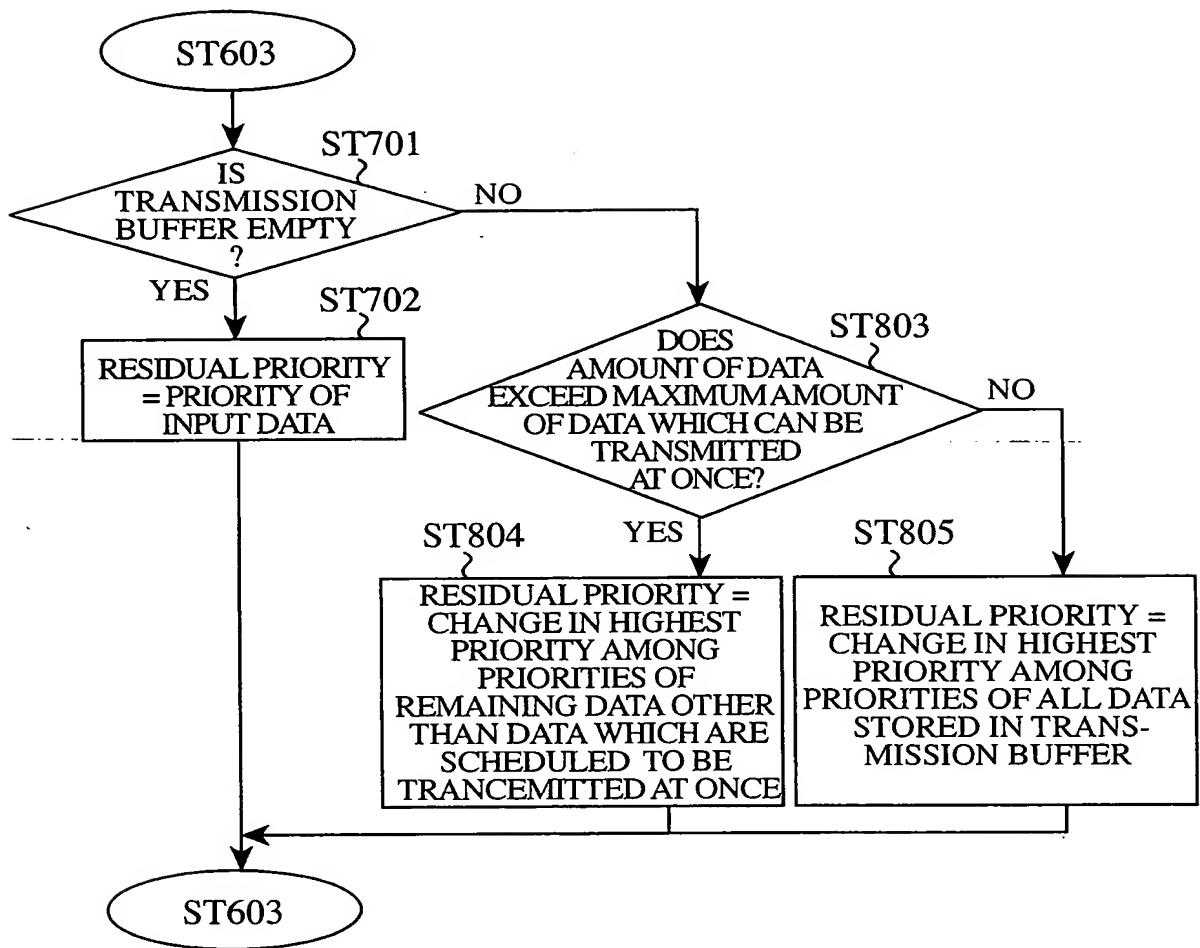


FIG.9

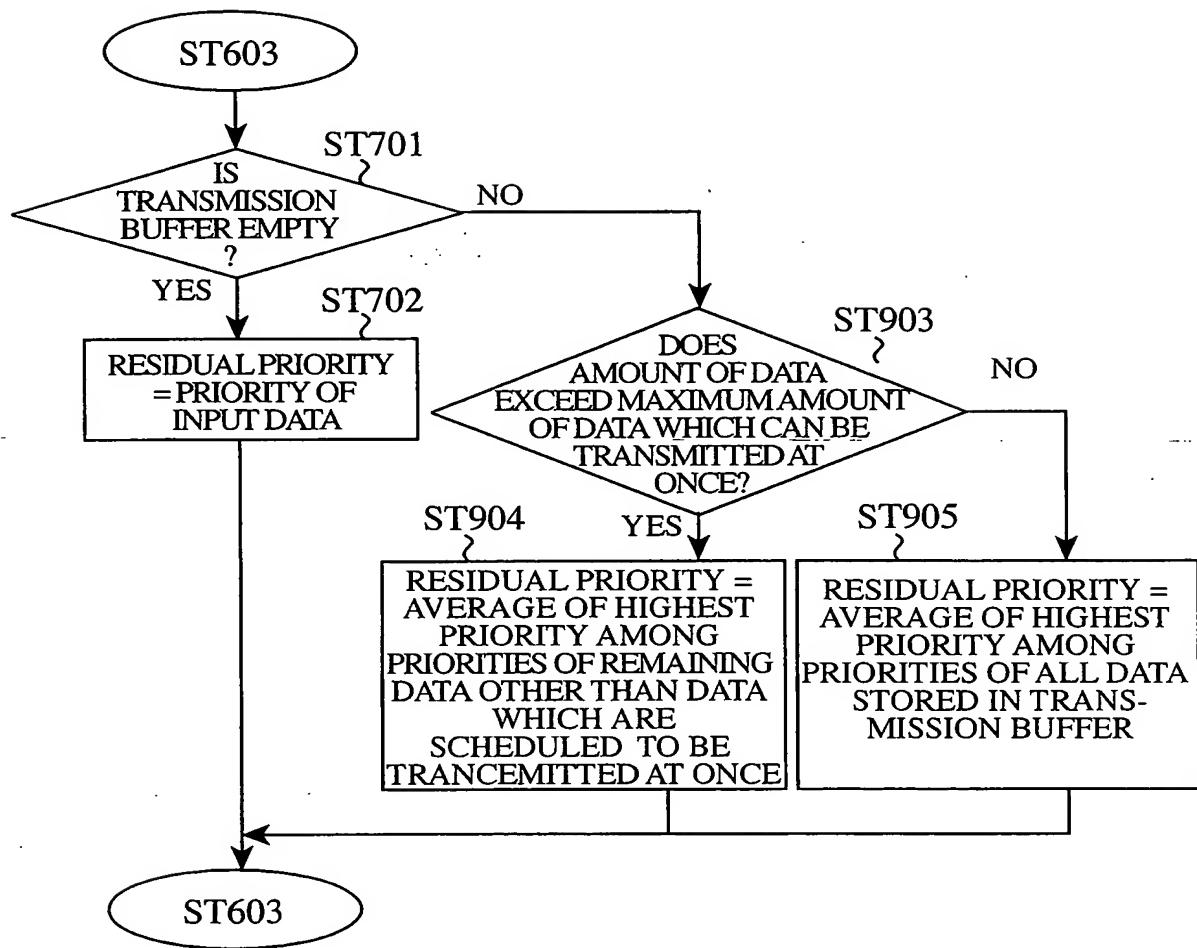


FIG.10

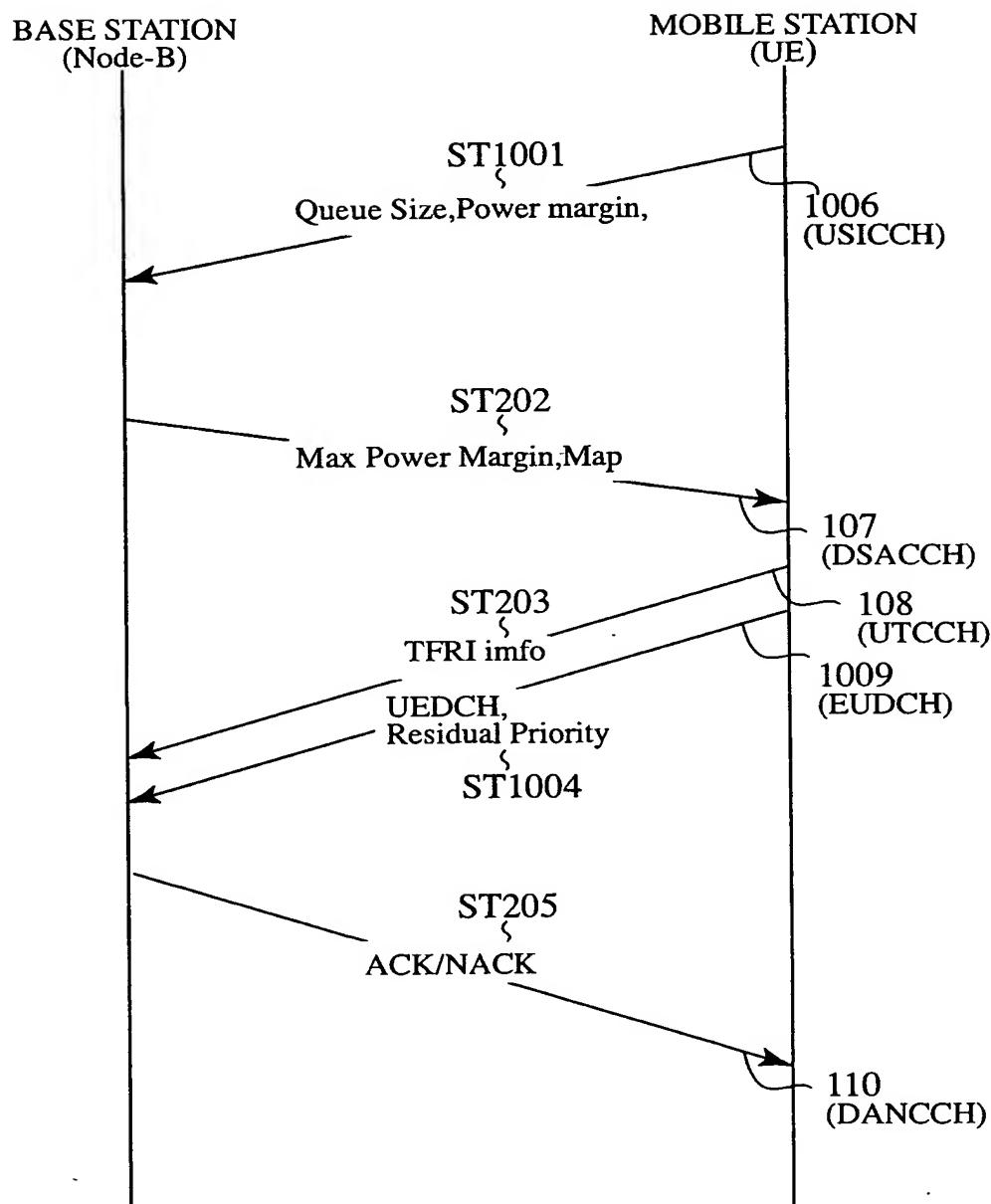


FIG.11

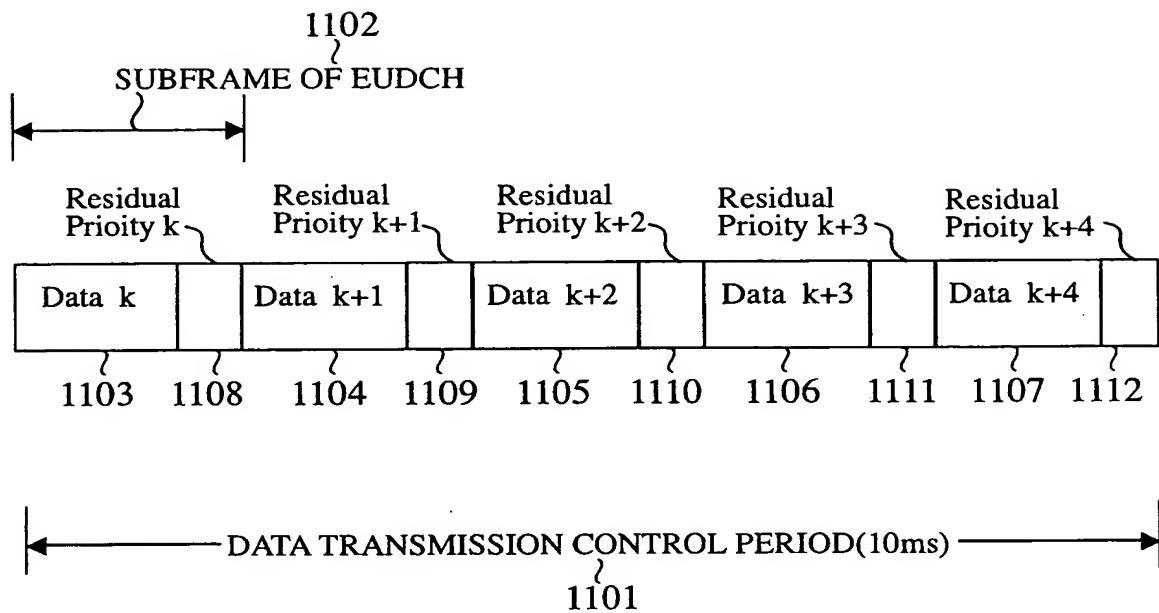


FIG. 12

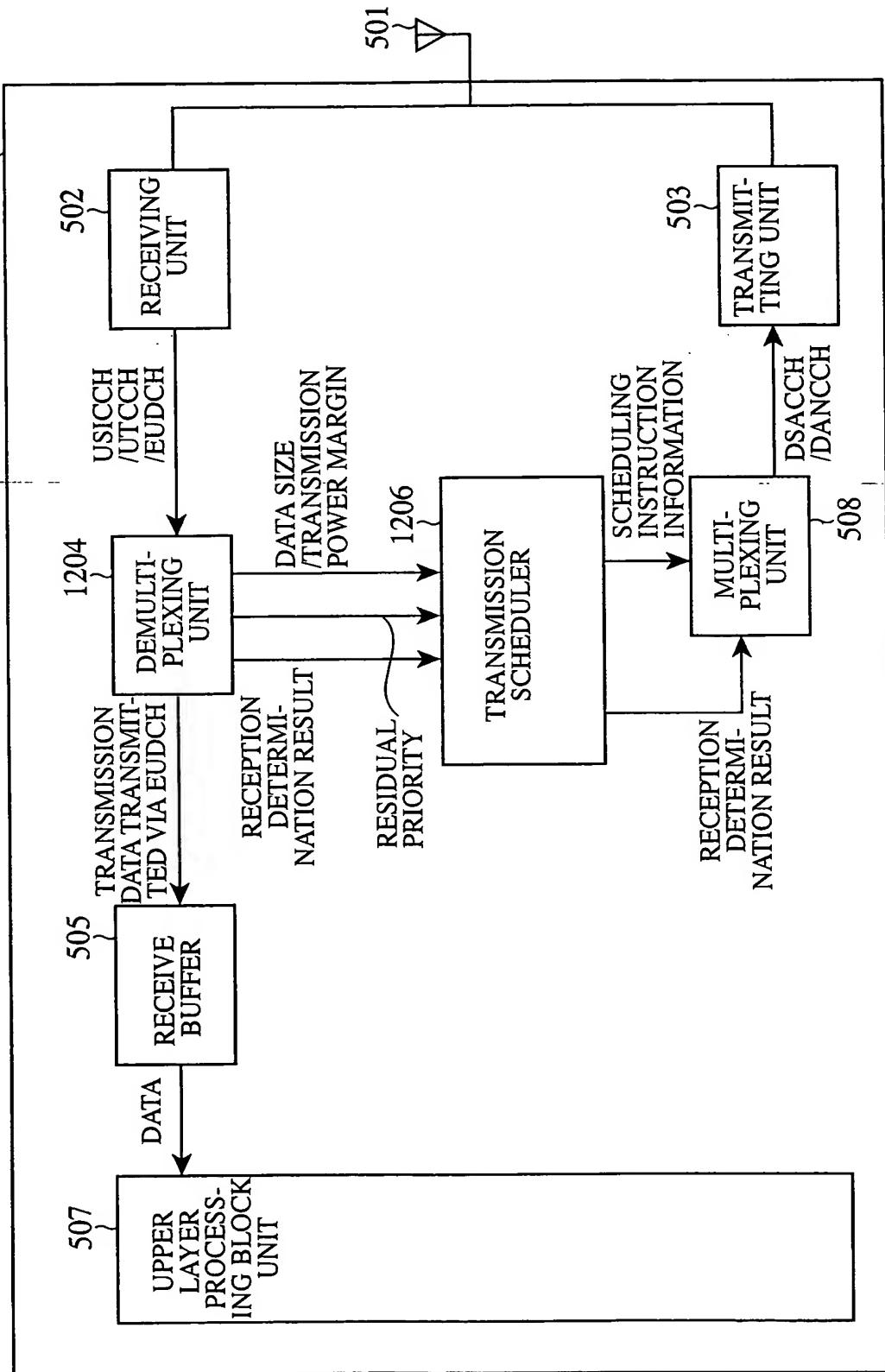


FIG.13

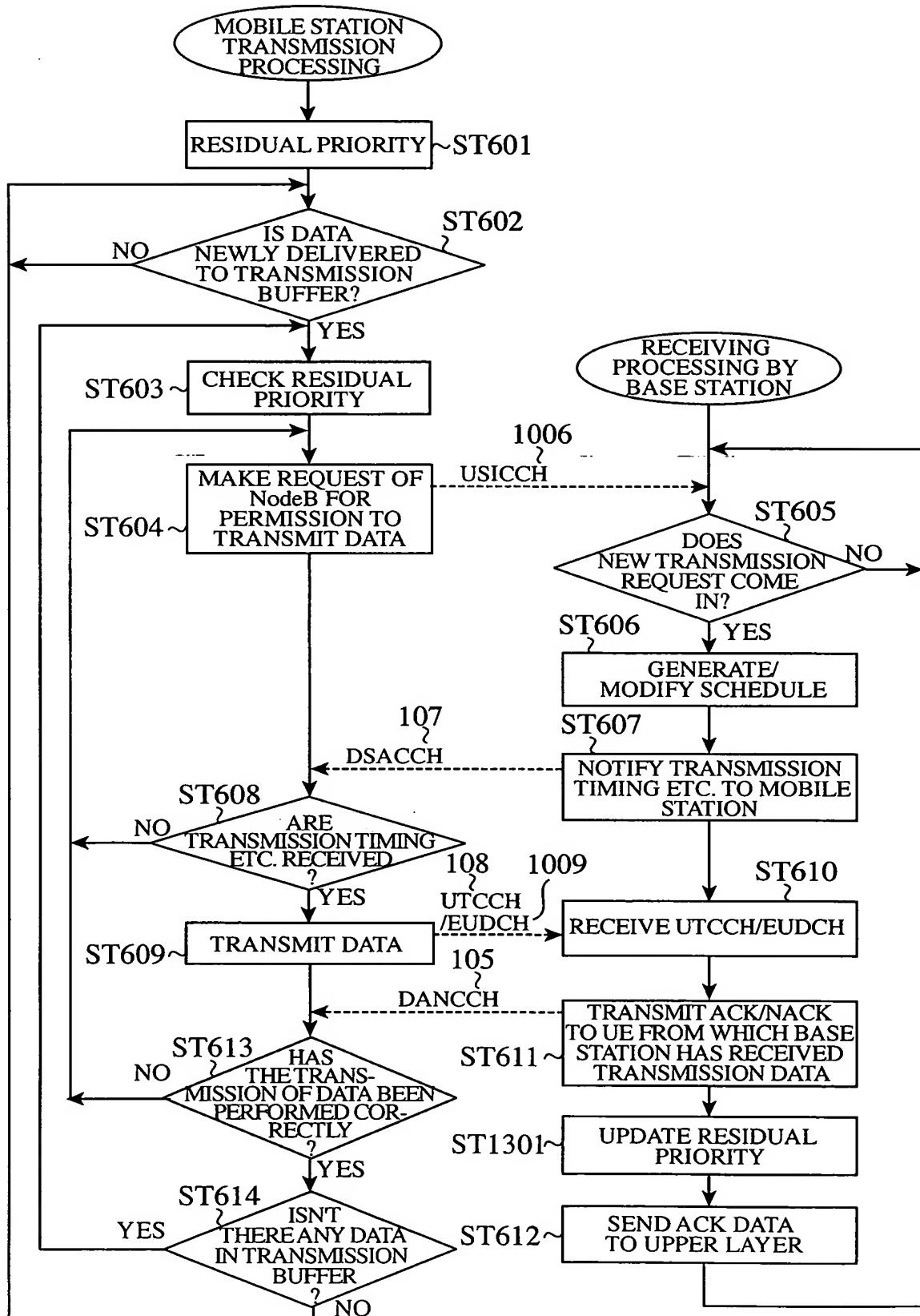


FIG.14

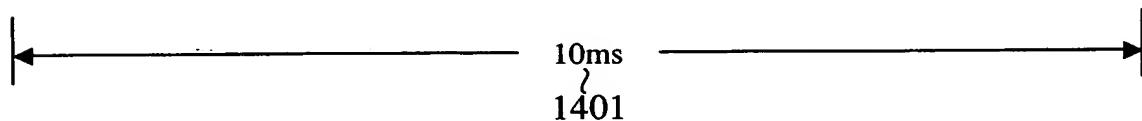
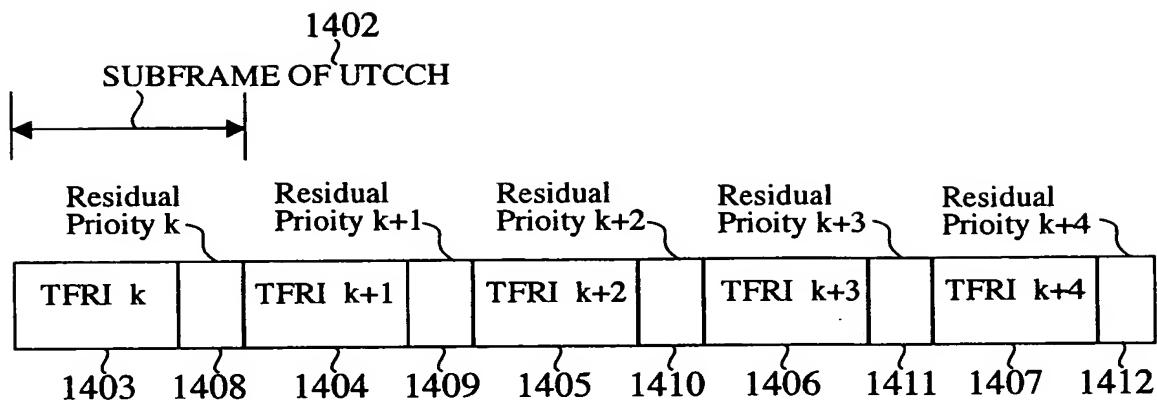


FIG.15

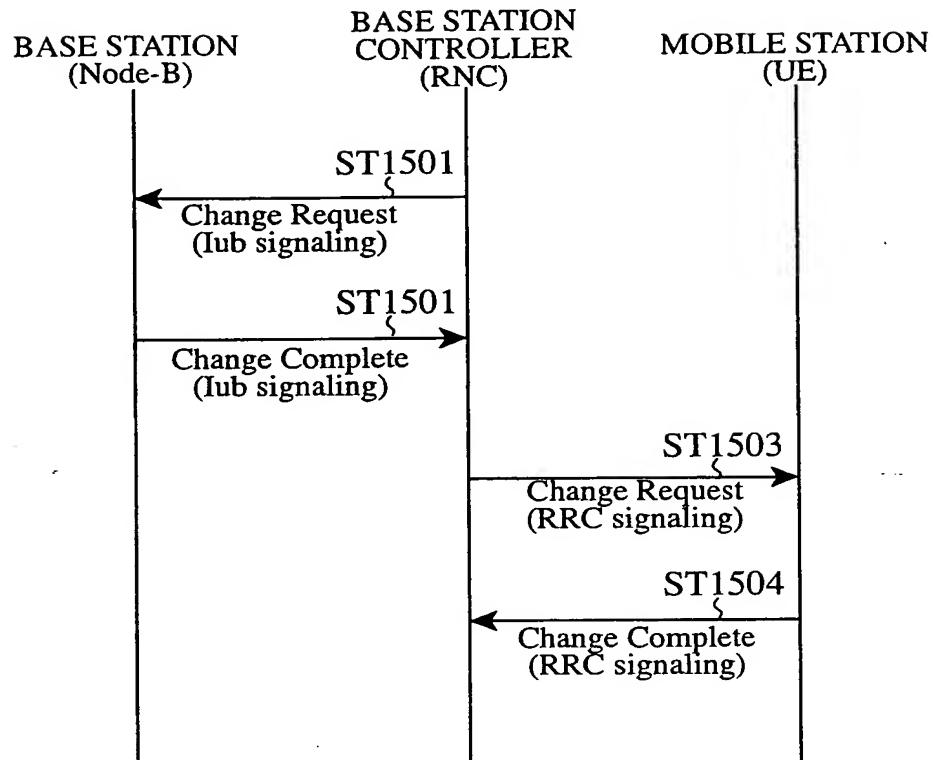


FIG.16

SIGNAL IDENTIFIER (Signalling index)	OPERATION MODE
0	USE OF PRIORITY
1	USE OF RESIDUAL PRIORITY
2	RESERVE OR OTHER OPERATION MODE

FIG. 17

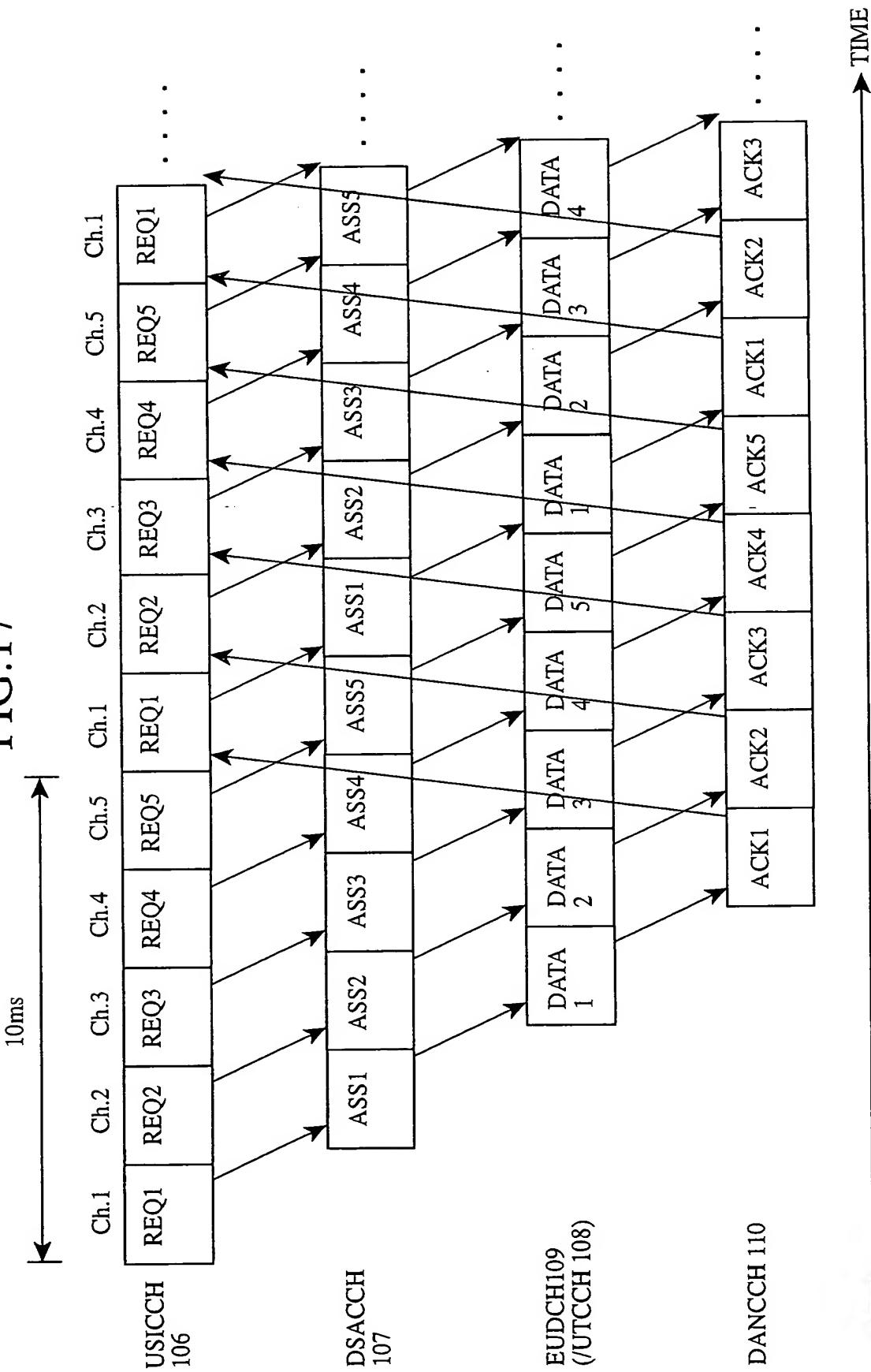


FIG.18

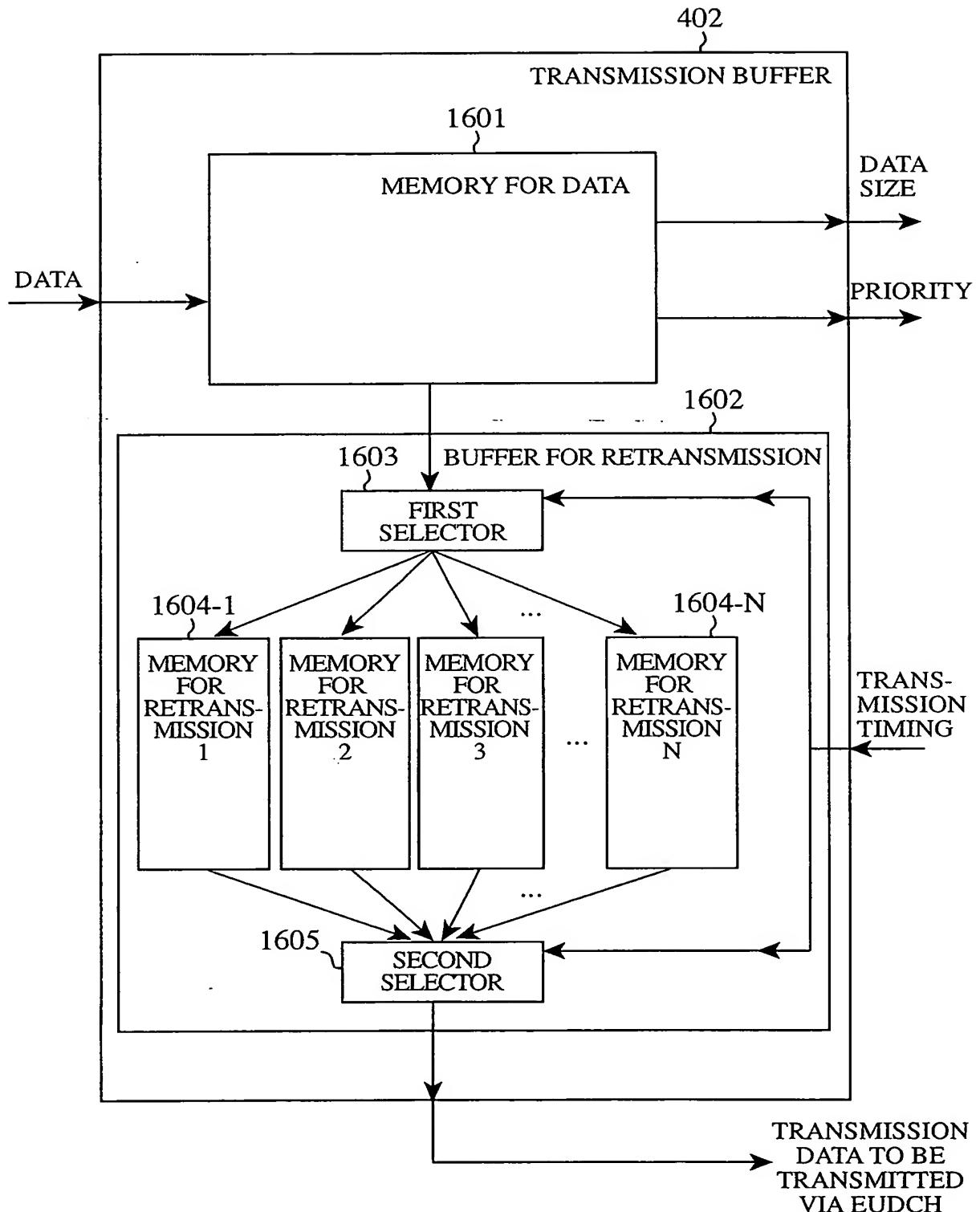


FIG.19

PRIORITIES OF DATA
STORED IN ALL
MEMORIES FOR

RETRANSMISSION
CYCLE

3,2,1	3,2,1	3,2,1	3,2,1	3,2,1	3,1	3,1	3,1	3,1	3,1	1
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Ch.1	Ch.2	Ch.3	Ch.4	Ch.5	Ch.1	Ch.2	Ch.3	Ch.4	Ch.5	Ch.1
------	------	------	------	------	------	------	------	------	------	------

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
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REQ1	REQ2	REQ3								
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REQ1	REQ2	REQ3								
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REQ1	REQ2	REQ3								
------	------	------	--	--	--	--	--	--	--	--

REQ1	REQ2	REQ3								
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DSACCH
107

EUDCH109
(UTCCCH 108)

DANCCCH 110

TIME

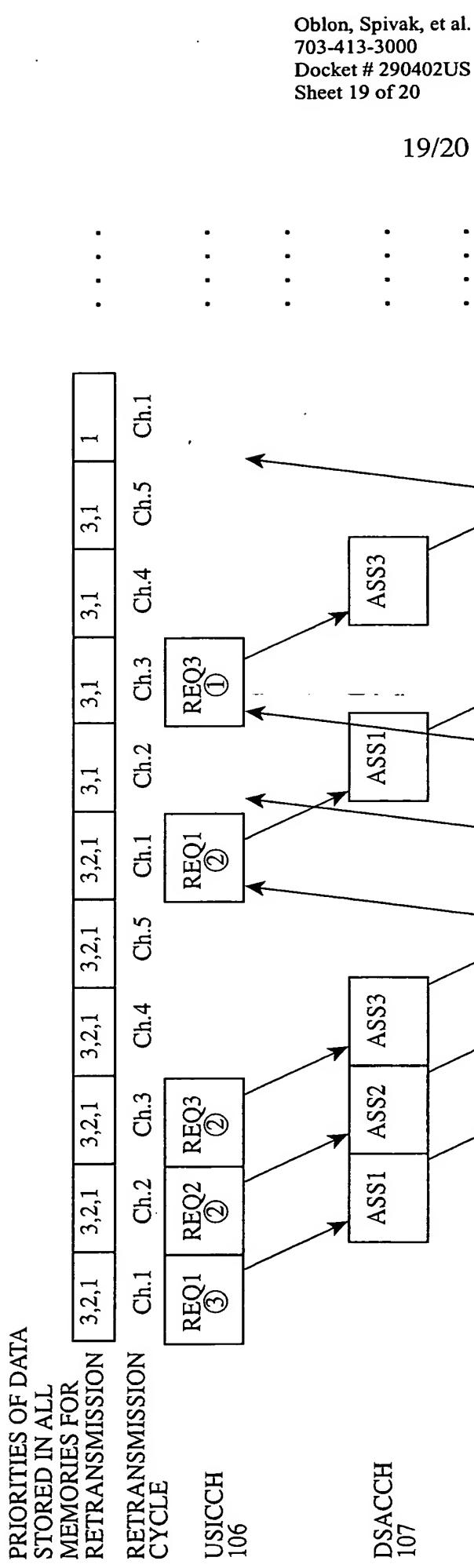


FIG.20

